



Double Balanced Transformerless JFET Mike Preamp Schematic

The input stage of the preamp circuit is a modified version of a circuit presented by Graeme John Cohen of Philips Microelectronics, Hendon Australia. The original paper is available from the AES preprint # 2106 .

The Balanced Floating Output Stage is a modified version of topology presented by George D Pontis, originally printed in the Hewlett-Packard Journal Pages 12-13, August, 1980.

If OPA-604 type opamps are used, use + and - 24 VDC Rails.