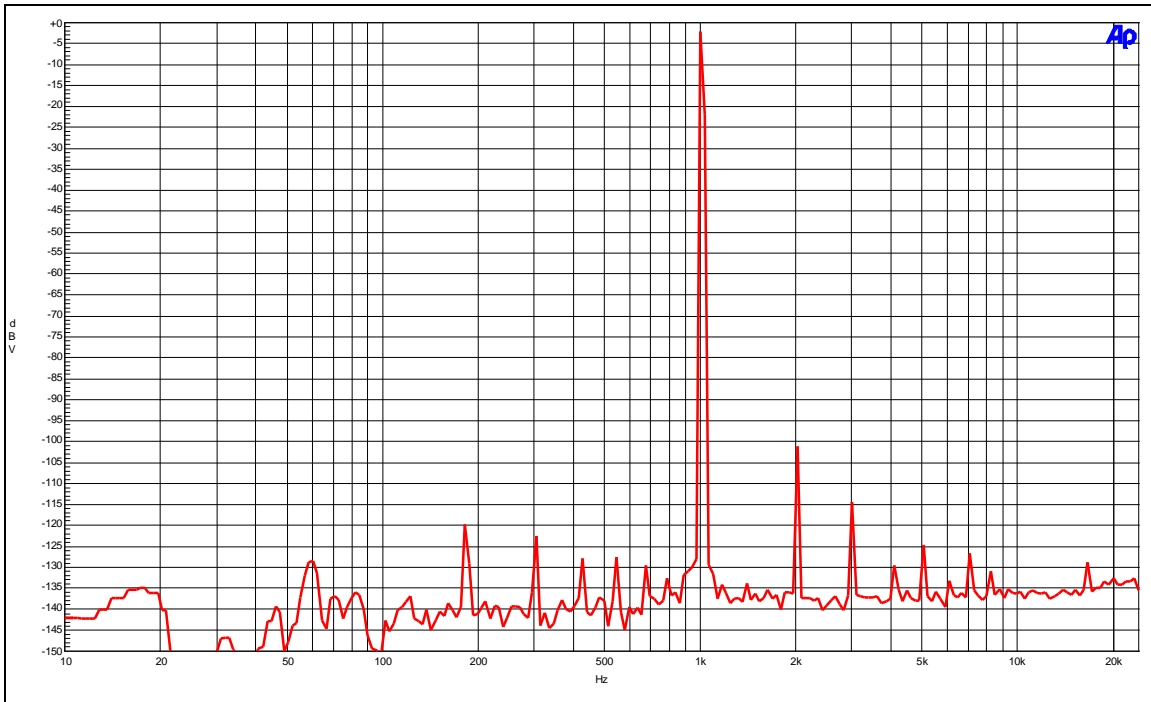
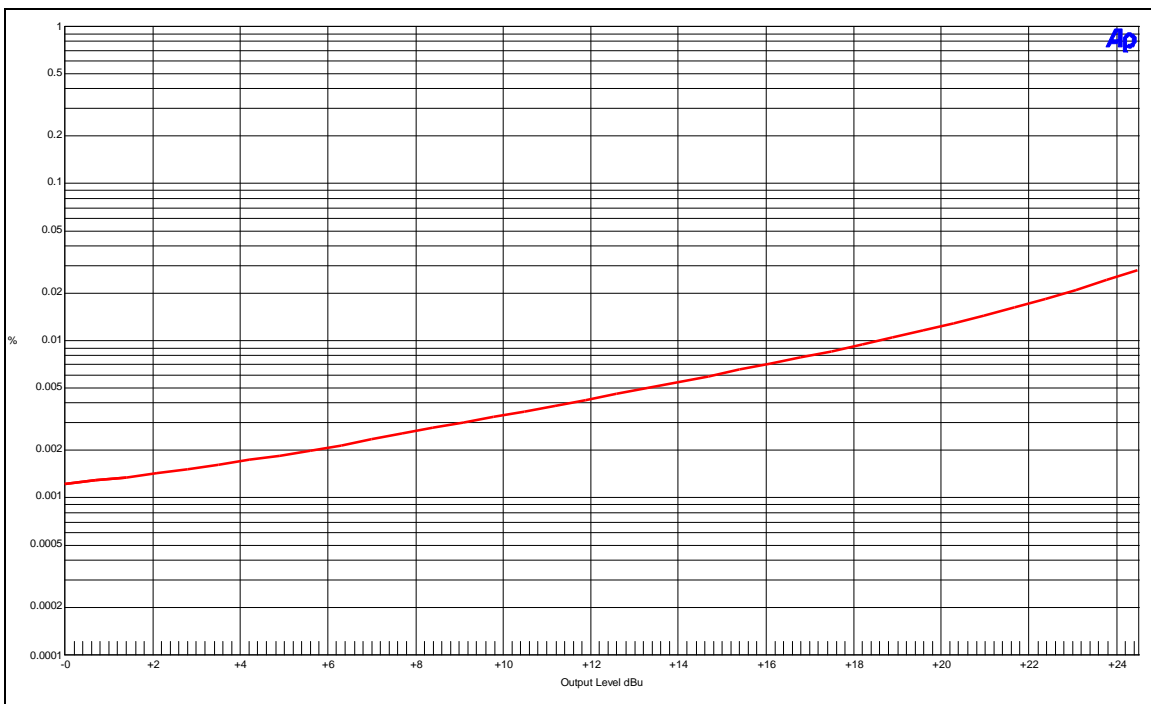


Preliminary JFET-993 Test Data and Specifications

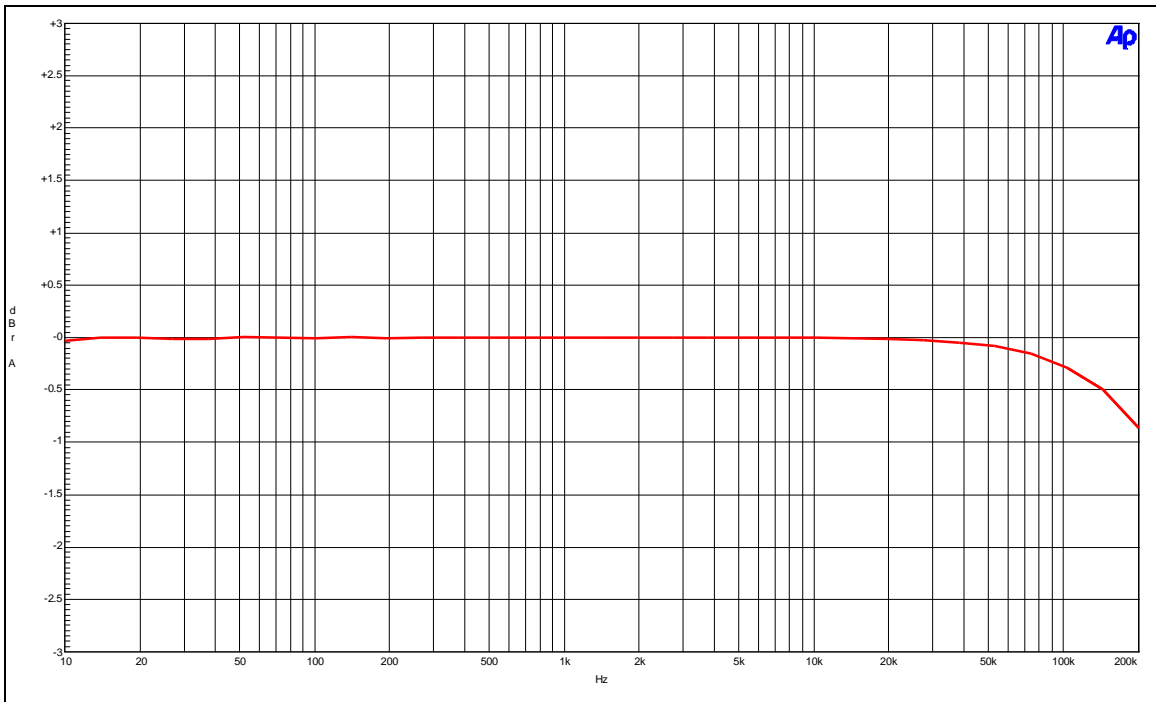
Note...All measurements done with Audio Precision System Two Cascade.
JFET-993 operating with +/- 24 VDC power supply rails.



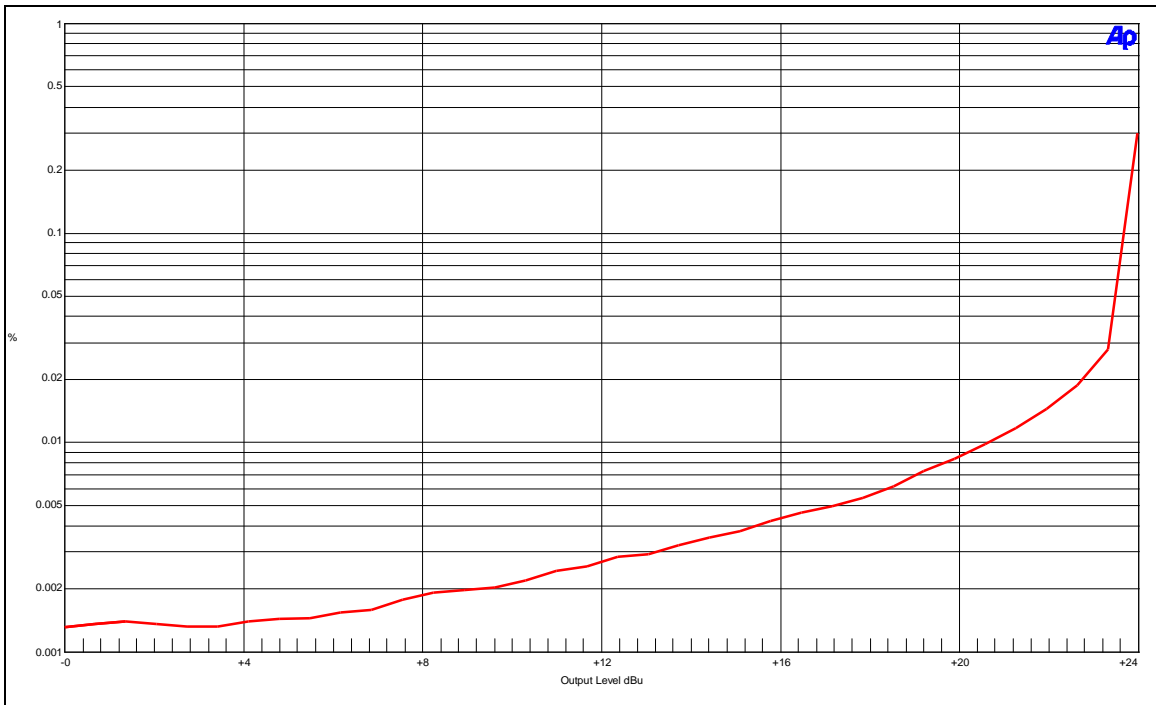
THD+N (%) at 0 dBu Output



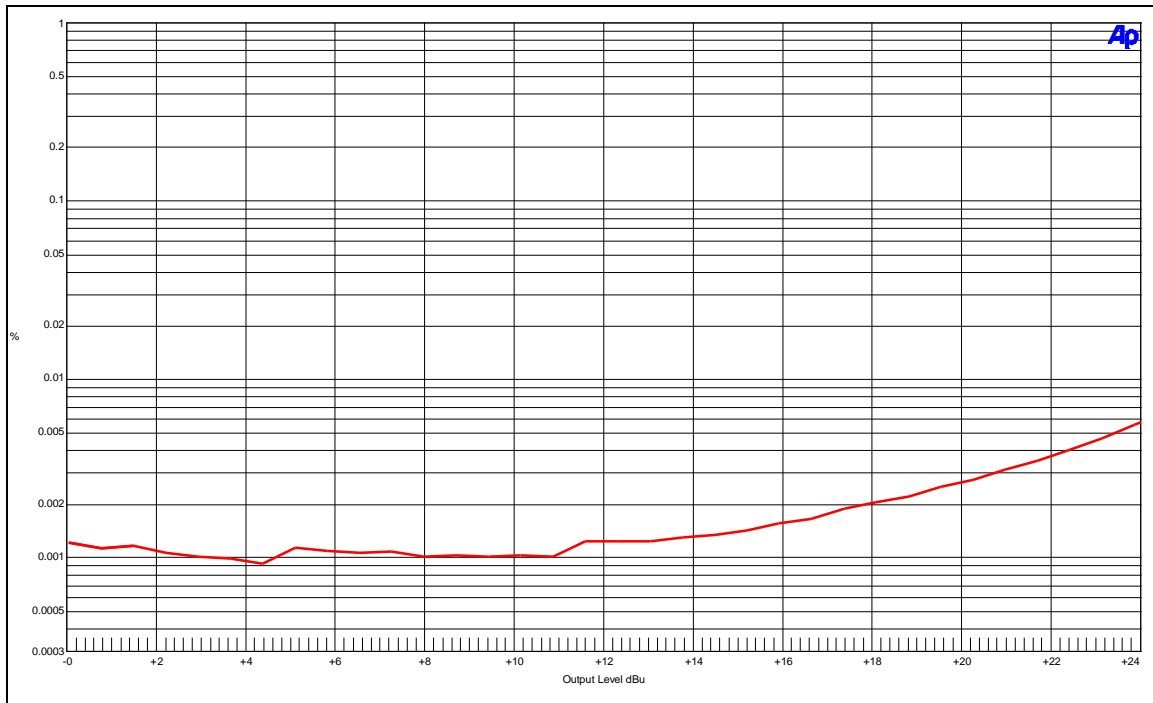
THD+N (%) vs Output Level into a 600-ohm load in parallel with .001 mfd



THD+N (%) vs Frequency @ 15 dB Closed-Loop Gain



IMD (DIN 4:1) vs Output Level @ 15 dB Gain into 600-ohm Load



DIM (Dynamic Intermodulation Distortion) vs Output Level

General Specifications:

Power Supply Voltage: +/- 12 VDC minimum +/- 25 VDC maximum

Power Supply Current: 30 milliamp each leg

Input Noise Voltage (EN): Better than 1.5nv/√Hz typical @ 1 KHz

Input Noise Figure (NF): Better than 1 dB @ 1 KHz typical

Input Impedance > 100 megohm

Input Capacitance < 30 pf

Closed Loop Output Impedance: < 1 ohm.

Output remains Class A at +24 dBu output into loads greater than 3.6 K ohms.

Output remains Class A into a 600 ohm load up to +10 dBu (approx).

Closed Loop -3 dB point > 3 MHz at unity gain: greater than 600 KHz at 15 dB gain.

Output DC offset < 20 millivolts at unity gain.